

ARCHANA P LOPES

ASSISTANT PROFESSOR

DEPARTMENT OF ELECTRONICS AND COMPUTER SCIENCE



9890599604



archana.lopes@gmail.com



Fr. C.R.C.E., Bandra, Mumbai-50

Career Goal

With my experience, knowledge and wisdom, my aim is to achieve a challenging and respected position in the Educational Field.

About me

I have taught following subjects as part of my experience C++, Java, Neural Network and Fuzzy Logic, Data structures and Algorithm, Python, Communication Networks, Machine Learning. With the educational, professional background in Field of Electronics I am comfortable teaching any subject related to Electronics, Information Technology and Computer Science.

Paper Publication

- 'Predicting the impact of Android Malicious Samples via Machine Learning' published in Second International Conference on Sustainable Technologies for Computational Intelligence (ICTSCI-2021), Springer publications
- 'Generating Hardware Fingerprint of FPGA using Physical Unclonable Function' published in International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering - (ICRIEECE), Bhubaneshwar, IEEE Xplore Digital Library
- 'Early detection of Heart Disease using BRANN' ,published in IJCRT, 10.1729/IJCRT.17983
- A paper on 'An Early Detection of Masses in Digitized Mammograms Using BPNN'in National conference on Recent Trends in Engineering and Technology, at Fr.Agnel

Education

- Ph.D. (Computer Science and Engineering)- Pursuing Ph.D. From K.L. University, Hyderabad in Computer Science and Engineering.
- M.E. (Electronics) from Fr. Conceicao Rodrigue College of Engineering, Fr. Agnel Ashram, BANDRA, Mumbai 400 050. (UNIVERSITY OF MUMBAI) July, 2009
- B.E. (Electronics) from Fr. Conceicao Rodrigue College of Engineering, Fr. Agnel Ashram, BANDRA, Mumbai 400 050. (UNIVERSITY OF MUMBAI) Dec, 2002

Language

English ———

Hindi

Marathi

Expertise

- Python
- Java
- Data Structures and Algorithms
- Machine Learning

Research Profile

Orchid ID

https://orcid.org/0000-0002-8454-9062



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Professional Summary

• FR. CONCEICAO RODRIGUES COLLEGE OF ENGINEERING, BANDRA (AUG, 2009-TILL DATE)

Designation: Assistant Professor in Electronics

Job Profile: Teaching

Subjects: C++, Core Java, Neural Network and Fuzzy Logic, Data Structures and Algorithms,

Python, Communication Networks

• VIDYAVARDHINI'S, A.V. COLLEGE, VASAI(W) (OCT, 2007 - AUG, 2009)

Designation: Lecturer in B.Sc(IT/CS)

Job Profile: Teaching

Subjects: C and C++, OS, SE, SQL

• ST. GONASALO GARCIA COLLEGE OF B.SC(I.T.), VASAI(W) (JUN, 2005 - MAR, 2007)

Designation: Lecturer in B.Sc(IT)

Job Profile: Teaching

Subjects: I.T., C++, Java, OS, SQL, SP, CM

• NUCSOFT PVT. LTD, ANDHERI (OCT, 2003 – AUG, 2004)

Designation: System engineer

Job Profile: Development, Support and Training

Subjects: Pro*C,Oracle

VIVA COLLEGE, VIRAR (JUN, 2003 – OCT, 2003)

Designation: Lecturer
Job Profile: Teaching
Subjects: ELECTRONICS

Language

Marathi -

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Minor Research Grants

Received Minor Research Grant from University of Mumbai for project on 'Stock Market Forecasting using Neural Network implementation on FPGA'

NPTEL/Coursera/ Udemy Courses

- Four-week NPTEL course on "Data Science with Python"
- Twelve-week NPTEL course on 'Problem Solving Through Programming in C'
- Four Weeks Coursera course on 'Python Programming: A Concise Introduction'
- Coursera Project on 'TensorFlow for AI: Get to Know Tensorflow'
- Coursera course on 'Introduction to TensorFlow for Artificial Intelligence, Machine Learning, and Deep Learning'
- Udemy Course on 'Data Structures and Algorithms using C'

STTP/FDP

- One-week STTP on 'Cyber Security: Individual, Technology and Research Trends' at Vidyavardhini's College of Engineering and Technology
- One Week STTP on, 'Technological Advancements in the field of Electronics and Telecommunication Engineering', at Vidyavardhini's College of Engineering and Technology
- One-week FDP on Python in association with spoken tutorial, IIT Mumbai, at Universal college of Engineering
- One Week STTP on, 'Vogue of Embedded System in association with VCET Texas instruments innovation lab', at Vidyavardhini's College of Engineering and Technology
- One- Week STTP on,' Data Science' at Fr.C.R.C.E.
- One-Week STTP on 'Artificial Intelligence' at Universal college of Engineering
- One- Week Training on 'Empowering teachers for online teaching', at St. Teresa's Institute of technology
- A one-week A one-week STTP on 'Image Processing Applications: Basics to Advanced' at St. Francis Institute of Technology
- A Two-week graded STTP on "Practical Approach of communication Networks"
- A 200 hours course on Embedded Systems by Thinklabs.
- A three-day workshop on "Neural Network and Fuzzy systems"

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