

MS. Binsy Joseph

Assistant Professor

Department of Electronics and Computer Science

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OBJECTIVES:

Seeking opportunities to take up more research projects and making use of the acquired knowledge for excelling in both teaching and research.

PROFESSIONAL EXPERIENCE:

Total Teaching Experience : 17 years

Research & Industrial Experience : 3 years

Feb 2006 –till Date Fr. Conceicao Rodrigues College of Engineering, Mumbai

Designation: 2011 -Till date :Assistant Professor

(from 2018 Selection Grade AGP -8000)

2006 – 2011 : Lecturer

2000-2003 : Industrial Experience

EDUCATIONAL QUALIFICATIONS:

2003 – 2005

M.Tech (Power Electronics) from Amrita Vishwa Vidyapeetam,Coimbatore.(**First Class with Distinction**)

1994 – 1998

B.Tech (Electronics & Communication Engineering) from LBS College of Engineering, Calicut University ,Kerala. (**FIRST class with Distinction 72.3%)**

Additional Qualification:

C-DAC Diploma in Advanced Computing - 1999

RESEARCH GRANTS:

Mumbai University Granted projects under Minor Research Grant (MRG) scheme:

1. Project Title: Smart Grid using solar power

Role: Principal investigator

Amount Granted: 25000/-

Year: 2016-17

Status: Completed

PUBLICATIONS:

Publications in International/national level Journals/ Conferences:

1. B Joseph, DV Bhoir ,” Design and Assessment of Electric Vehicle Performance Parameters based on Drive Cycle”, International Conference on Automation, Computing and Communication (ICACC-2021)July 14-15, 2021. ITM Web of Conferences, DOI: 10.1051/itmconf/20214001007.
2. B Joseph, DV Bhoir ,”Design and Simulation of Wireless Power Transfer for Electric Vehicle”, International Conference on Advances in Computing, Communication and Control (ICAC3)2019, DOI: [10.1109/ICAC347590.2019.9036745](https://doi.org/10.1109/ICAC347590.2019.9036745)
3. F Shaikh, B Joseph,”Simulation of synchronous reference frame PLL for grid synchronization using Simulink ”, International Conference on Advances in Computing, Communication and Control (ICAC3)2017, DOI: [10.1109/ICAC3.2017.8318790](https://doi.org/10.1109/ICAC3.2017.8318790)
4. Binsy Joseph,” Harmonic Reduction in Three Phase Rectifier Using Current Injection”, [Communications in Computer and Information Science](#) book series (CCIS,volume 296) 2013 ,pages: 269-274 DOI: 10.1007/978-3-642-35864-7_38.
5. Binsy Joseph, Ahmad Nadeem,” Multilevel Inverters with Its Control and Power Quality Parameters”, Issue 1 ,vol 3, Imperial Journal of Interdisciplinary Research, 2017, ISSN:2454-1362.
6. Anju Yadav, Binsy Joseph,”Sinusoidal Current Control based Shunt Active Power Filter for current Harmonics Reduction”, International Journal of Science and Research, Issue 7 ,vol 4,2015, ISSN 2319-7064.
7. Binsy Joseph,” Sinusoidal Current Control based Shunt Active Power Filter for current Harmonics Reduction”,International Journal of Global Technology Initiatives, Issue 1 ,vol 1,2012, ISSN No 2277-6591.

RESEARCH AREAS:-

1. Power Electronics System Design
2. Electric Vehicle Battery Management
3. Data driven methods for Battery Prediction
4. Smart Grid and Renewable System Integration.
5. Computer Networks

EXPERT AREAS :-

1. System simulation and Modelling
2. Matlab /Python/Orcad/PSpice/Scilab etc
3. Python, Java, C++, C, Assembly Language programming.