***Practical Plan***

***BE Electronics Semester VII***

 ***Faculty :Narayanan Kallingal***

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| CLASS | BE Electronics Semester VII |
| Academic Term | July – November 2020 (2020 – 21) |
| Subject | **Digital Signal Processing Laboratory (ELXL703)** |
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| ***Title of Experiments*** |
| Sr.No. | Title | Module |
| 1 | DFT and IDFT | DFT |
| 2 | BUTTERWORTH FILTER  | IIR Filter Design |
| 3 | CHEBYSHEV FILTER | IIR Filter Design |
| 4 | DESIGN OF FIR FILTER USING WINDOW METHOD | FIR Filter Design |
| 5 | EFFECT OF FINITE WORD LENGTH | Effect of Finite Word Length |
| 6 | MULTIRATE SIGNAL PROCESSING | Multi rate DSP |
| 7 | Mini Project |  |
| ***Newly Added Experiments*** |
| 1 | EFFECT OF FINITE WORD LENGTH | Effect of Finite Word Length |
| ***Practical Plan*** |
| *Experiment No. 1 Compute DFT and its Inverse* |
| *Batch* | *Dates* |  |
| *Planned* | *Actual* |
|  |  | 18 – 08 – 2020  |  |
| *Experiment No. 2 Symmetry Properties of DFT* |
|  |  | *01 – 09 – 2020*  |  |
| *Experiment No. 3 Analog Butterworth Filter* |
|  |  | 22 – 09 – 2020  |  |
| *Experiment No. 4 Design of FIR Filter with Hamming window* |
|  |  | 06 – 10 – 2020  |  |
| *Experiment No. 5 Decimation and Interpolation* |
|  |  | 28 – 10 – 2020  |  |
| *Experiment No. 6 Effect of Finite Word Length* |
|  |  | 11 – 11 – 2020  |  |

***Mini Project***

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|  **Batch** | **Planned** | **Actual** |  |
|  |  | 19 – 11 – 2020  |  |

1. Minimum 6 experiments and one course project must be submitted by each student.
2. Simulation tools like Matlab/Scilab can be used.
3. Processor based experiments/mini projects can be included. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced

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| **Submitted By**  | **Approved By** |
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| Prof. Narayanan Kallingal | i) Prof. Narayanan Kallingal Sign: |
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| Sign: | ii) Dr. D V Bhoir Sign: |
|  |  |
|   | iii) Prof. Shilpa Patil Sign: |
|  | iv) Prof. Monica Khanore Sign: |
|  |  |
| **Date of Submission:**  | **Date of Approval:** |
|  |
| **Remarks by PAC (if any)** |
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