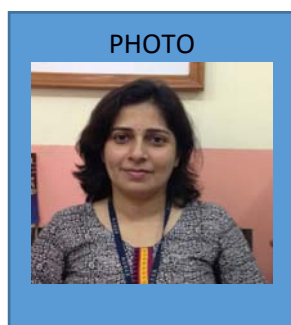


FACULTY PROFILE

(Kindly give the information in plain text format. Do not send in tabular form)



NAME : Shilpa J. Patil
DESIGNATION : Associate Professor
QUALIFICATION : M.E. (Power Systems)
EMAIL : shilpa@frcrce.ac.in
PHONE : 022 67114111
AREA OF INTEREST : Power electronics, Digital Electronics

PROFESSIONAL BACKGROUND :

- Worked as Lecture in Fr. Conceicao Rodrigues College of Engg from January 2017 to August 2010
- Working as Associate Professor in Fr. Conceicao Rodrigues College of Engg. from September 2010 till date

EDUCATIONAL DETAILS : M.E (Power Systems) Pune University
B.E. (Electrical Engg.) Pune University

ADMINISTRATIVE BACKGROUND :

- Member of Programme Assessment Committee of Department of Electronics
- Contact chair in the International Conference on 'Advances in Computing, Communication and Control'-2017
- Fr. Agnel Technical Complex ISTE Chapter Treasurer
- Programme co chair in the International Conference on 'Advances in Computing, Communication and Control'-2015
- Publication chair in the International Conference on 'Advances in Computing, Communication and Control'-2013
- M.E. Programme Coordinator since 2013 till date
- Laboratory Incharge of Power Electronics Laboratory

Ph.D. SUPERVISED : Nil

RESEARCH GRANT PROJECTS/PATENTS : Nil

PUBLICATIONS :

- Prasanna Sagdeo, Shilpa Patil, Vipinkumar Meshram, "SVPWM controller for three phase inverter using PI controller operating under nonlinear load", in International Journal of Advance Research in Computer and Communication Engineering, vol.4, issue 10, Oct. 2015.
- Najib Ghatte, Shilpa Patil, Deepak Bhoir, " Single Precision Floating Point Division in Fifth IRF Proc. International Conference on Software Technology and Computer Engineering, Goa, pp. 34-38, Aug. 2014.
- Najib Ghatte, Shilpa Patil, Deepak Bhoir, " Single Precision Floating Point Square Root Computation" in Fifth IRF Proc. International Conference on Software Technology and Computer Engineering, Goa, pp. 29-33, Aug. 2014.
- Najib Ghatte, Shilpa Patil, Deepak Bhoir, "Double Precision Floating Point Square Root Computation" in International Journal of Engineering Trends and Technology(IJETT), V13(6), pp. 294-298 Jul. 2014.
- Najib Ghatte, Shilpa Patil, Deepak Bhoir, "Floating Point Engine using VHDL" in International Journal of Engineering Trends and Technology(IJETT), V8(4), pp. 198-203 Feb. 2014, 198-203 ISSN:2231-5381.
- Najib Ghatte, Shilpa Patil, Deepak Bhoir, "High Speed ALU Design using Vedic Sutras" in Pragyanaam'14 Proc. Int. Conference on Recent Trends in Computer and Electronics Engineering, Mumbai, 2014 pp. 227-232.
- Manjusha Kulkarni, Shilpa Patil, Sapna Prabhu, "DS-CDMA transmitter and receiver in VHDL for FPGA", International Conference on Advances in Communication and Computing Technologies, August 2013.

BOOKS AUTHORED : Nil

TRAINING PROGRAMS CONDUCTED :

- Coordinator of 2 weeks ISTE approved Short Term Training Programme on 'Simulation: Theory to Practice' in 2012-13
- Coordinator of 1 week ISTE approved Short Term Training Programme on 'Robotics and its applications' in 2007-08

PARTICIPATION IN TRAINING PROGRAM :

- 1 week workshop on “ Innovative methods for Outcome Based Teaching” in May 2018
- 2 days workshop on “FPGA programming Practices” in February 2018
- 2 days workshop on “ Innovative Research in Pedagogy with Mini MOOCs blended with Instruction Strategies to enhance quality in education” in July 2017
- 2 days Hands on training on “System Simulation Tools” in September 2016
- 2 days Hands on training on “Remote learning Technology” in February 2016
- 2 days Hands on training on “Teaching Power Electronics with MATLAB/SIMULINK” in July 15
- 1 week ISTE approved STTP on “Emerging Trends in Power Electronics Application” in July 15
- 1 week ISTE approved STTP on “Outcome based Education” in July 14
- 2 days Faculty development programme on “Techniques for effective Technical Paper writing” in August 2013
- 1 week ISTE approved STTP on “Mission 10X; workshop on effective teaching methodology conducted by Wipro” in August 2011
- 1 week ISTE approved STTP on “The Third Eye: A new angle of vision towards Research” in December 2011
- 1 week ISTE approved STTP on “Embedded Systems and real time applications” in 2009
- 1 week ISTE approved STTP on “Emerging trends in Embedded System Design” in 2008
- 2 weeks ISTE approved STTP on “Micro controller and Embedded Systems Design” in 2006
- 1 week ISTE approved STTP on “VLSI and Embedded Systems” in 2005
- 2 weeks ISTE approved STTP on “Networking :Theory to practice” in 2003

INTERACTION WITH OUTSIDE WORLD : Member of Committee for setting Syllabus of Electronics Engineering courses , Mumbai University

PROFESSIONAL AFFILIATIONS : Life membership of ISTE

OTHERS DETAILS : **(Any other details like award or achievements not included in any of the above heads)**