FACULTY PROFILE

NAME : Shilpa J. Patil

DESIGNATION : Assistant Professor

QUALIFICATION : M.E. (Power Systems)

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AREA OF INTEREST : Digital Electronics, Power electronics,

PROFESSIONAL BACKGROUND :

- Worked as Lecture in Fr. Conceicao Rodrigues College of Engineering from January 1996 to August 2010
- Working as Assistant Professor in Fr. Conceicao Rodrigues College of Engineering from September 2010 till date

EDUCATIONAL DETAILS : M.E (Power Systems) Pune University

B.E. (Electrical Engg.) Pune University

ADMINISTRATIVE BACKGROUND :

- Member of Institute Quality Assessment Cell (IQAC)
- Program Coordinator of B.E. ECS Program in the institute (From July 2019)
- Contact chair in in the International Conference on "Advances in Computing, Communication and Control-2017, 2019"
- Programme co-chair in the International Conference on 'Advances in Computing, Communication and Control'-2015
- Publication chair in the International Conference on 'Advances in Computing, Communication and Control'-2013
- Fr. Agnel Technical Complex ISTE Chapter Treasurer
- M.E. Electronics Program Coordinator in the institute (2013-2019)
- Member of Unfair means Enquiry Committee in the Institute

PUBLICATIONS :

- Avilash Sawant, Shilpa Patil, K. Aurobindo, “Waveform generator for RADAR using FPGA”, in IEEE international Conference on Recent Trends in Electronics, Informanation and communicationTechnology, May 18th, 2018
- PrasannaSagdeo, Shilpa Patil, VipinkumarMeshram, "SVPWM controller for three phase inverter using PI controller operating under nonlinear load", in International Journal of Advance Research in Computer and Communication Engineering, vol.4, issue 10, Oct. 2015
- N

  ajib Ghatte, Shilpa Patil, Deepak Bhoir, "Single Precision Floating Point Division

  in Fifth IRF Proc. International Conference on Software Technology and Computer Engineering, Goa, pp. 34-38, Aug. 2014
- NajibGhatte, Shilpa Patil, Deepak Bhoir, "Single Precision Floating Point Square Root Computation" inFifth IRF Proc. International Conference on Software Technology and Computer Engineering, Goa, pp. 29-33, Aug. 2014
- NajibGhatte, Shilpa Patil, Deepak Bhoir, "Double Precision Floating Point Square Root Computation" in International Journal of Engineering Trends and Technology(IJETT), V13(6), pp. 294-298 Jul. 2014
- NajibGhatte, Shilpa Patil, Deepak Bhoir, "Floating Point Engine using VHDL" inInternational Journal of Engineering Trends and Technology(IJETT), V8(4), pp. 198-203 Feb. 2014, 198-203 ISSN:2231-5381
- NajibGhatte, Shilpa Patil, Deepak Bhoir, “High Speed ALU Design using Vedic Sutras” in Pragyanam’14 Proc. Int. Conference on Recent Trends in Computer and Electronics Engineering,Mumbai, 2014 pp. 227-232
- Manjusha Kulkarni, Shilpa Patil, SapnaPrabhu, "DS-CDMA transmitter and receiver in VHDL for FPGA", International Conference on Advances in Communication and Computing Technologies, August 2013

TRAINING PROGRAMS CONDUCTED :

- Coordinator of 2 weeks ISTE approved Short Term Training Programme on ‘Simulation: Theory to Practice’ in 2012-13
- Coordinator of 1 week ISTE approved Short Term Training Programme on ‘Robotics and its applications’ in 2007-08

PARTICIPATION IN TRAINING PROGRAM:

- 12 weeks NPTEL course on 'Programming in JAVA' in July-October 2022
- 1 week STTP on 'Cyber security: Individual, Technology and Research Trend' in June 2021
- 1 week STTP on 'Modern educational tools and pedagogical practices for online teaching' in June 2020
- 1 week STTP on 'Moodle: Learning Management System' in March 2020
- 12 weeks NPTEL online course on "Introduction to Internet of Things" during January-April 2019
- 8 weeks NPTEL online course on "Introduction to Research" August-September 2018
- 1 week STTP on 'Innovative teaching learning practices to achieve Outcome Based Education and Accreditation' in May 2018

INTERACTION WITH OUTSIDE WORLD :

- Member of Committee for setting Syllabus of Electronics Engineering and Electronics and Computer science courses, Mumbai University