

PROF. SAMRIN PABREKAR



Assistant Professor

Department of Computer Science Engineering
Fr. Conceicao Rodrigues College of Engineering
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Name : PROF. SAMRIN PABREKAR

Qualification : M.E.(Electronics & Telecomm Engineering), Mumbai University (2013)
B.E.(Electronics), Mumbai University (2013)

Designation :Assistant Professor

Institute : Department of Computer Science and Engineering,
Fr. Conceicao Rodrigues college of Engineering, Bandra, Mumbai, India.

Date of Birth:	19 th August, 2026	Gender:	Female
Marital Status:	Married	Nationality:	Indian

Correspondence address: 802, Modern Enclave, R.B.Chandorkar Marg. Mumbai Central (East), Agripada.Mumbai-400011.

WORKEXPERIENCE

Organization	Designation	Duration
M.H.Saboo Siddik Polytechnic	Visiting Lecturer (Compute rEngg.)	Dec 2015till March2017
M.H.Saboo Siddik Polytechnic	Visiting Lecturer (Electronics &Telecomm Engg.)	Feb2017 till April2017
M.H.Saboo Siddik Polytechnic	Lecturer (Dept. of Electronics &Telecomm Engg.)	June2017 till May2023
M.H. Saboo Siddik Polytechnic	I/cHOD, CivilEngg (B)Dept.	June2022 till May2023
Rajiv Gandhi Institute of Technology, Mumbai	Assistant Professor (Dept. of Artificial Intelligence and Data Science)	August 2023 till July 2026
Fr.Conceicao Rodrigues college of Engineering, Bandra, Mumbai,	Assistant Professor (Dept. of Computer Science and Engineering)	August 2026 till date

EDUCATION

Qualification	University/Board	NameofCollege	Month & Year of Passing
M.E (Electronics & Telecomm)	Mumbai	Vidyalankar Institute of Technology	January2017
B.E (Electronics)	Mumbai	M.H. Saboo Siddik College of Engineering	May 2013
HSC	Maharashtra State Board, Pune	Sophia College	Feb 2009
SSC	Maharashtra State Board, Pune	St. Joseph's High School	March2007

KEY DELIVERABLES

➤ **At M.H. Saboo Siddik Polytechnic (Electronics&TelecommunicationDept)**

- To involve in the admission work to guide students and their parents with the Centralized Admission Process (CAP) and other related activities.
- To involve in the process of examination as an invigilator to ensure proper conduct of exams.
- To involve in the academic work regarding activities of the National Board of Accreditation (NBA).
- To involve in the process of examination as QC in charge.

➤ **At Rajiv Gandhi Institute of Technology (Artificial Intelligence& Data ScienceDept)**

- Mini Project Coordinator for Second Year and Third Year students .
- Mini Project Guide for S.E and T.E students.
- Student Mentoring Coordinator for the Department.
- TimeTable and Load Distribution Coordinator of the Department.
- Understudy for Mumbai University Examination Nov-23
- Laboratory In charge in Artificial Intelligence and Data Science Department
- Senior Supervisor for Mumbai University Examination May-June,2026

AREAS OF INTEREST

- Computer Graphics
- Computer Network
- Software Engineering and Project Management
- Image and Video Processing
- Embedded System
- Basis Electrical And Electronics Engineering
- Agile Software Development Tool

TECHNICAL COMPETENCIES

- ✓ Hands on experience of designing 3D Nanowire Device Structure using TCAD.
- ✓ Python Coding for Simulation of 22nm nanowire.
- ✓ Hands on experience on Programming 8051 using Keil μ Vision.

CERTIFICATION

- ✓ Holding the Participation Certificate in Poster Presentation at the First National Research Symposium by Vidyalankar Institute of Technology.
- ✓ Holding the Certificate of Excellence, being awarded Consolation prize in Post Graduate Category in Poster Presentation at the First National Research Symposium by Vidyalankar Institute Of Technology
- ✓ Holding certificate of participation in the 2-days Workshop conducted by Cadre Design System at Vidyalankar Institute Of Technology on “Visual TCAD (VTCAD-2016) and Genius Device Simulator”.

PAPER PUBLICATION

- Ansari Samrin, Sangeeta Joshi, “Nanowire: An Innovative Approach For 22nm Technology Node”, poster in First National Research Symposium on RACEM -2015, VIT Mumbai.
- Ansari Samrin, Ashish Bait, Subha Subramaniam, Sangeeta Joshi, Geetha Narayan, R.N. Awale “Design Optimization of Nanowire Transistor with Impact of High-K at sub-22nm”, in The 3rd International Conference on Emerging Electronics (ICEE 2016) organized by IIT Bombay.

